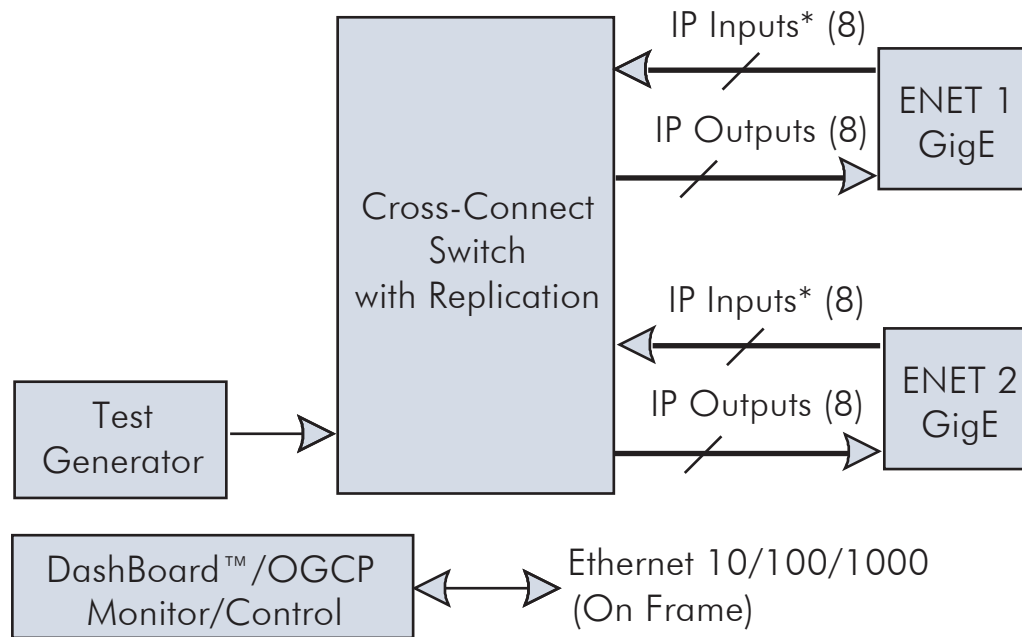


# 9990-RTR-MPEG-IP Block Diagram

Streaming IP (MPEG-TS) Reclocking Distribution Amplifier and Router



\* On model 9990-RTR-8X16-MPEG-IP, number of input ports is limited to eight (total), which can be distributed across either of both GigE interfaces.